

Notice of References Cited	Application/Control No. 10/715,440		Applicant(s)/Patent Under Reexamination MAHER ET AL.	
	Examiner KIDEST BAHTA		Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Suzuazu et al., A Microprocessor with a 128-Bit CPU, Ten Floating MAC's Four Floing-Point Dividers, and an MPEG-2 Deoder, November 1999, IEEE journal of solid Steat Circuit. Vol. 34, Page 1608-1618.			
	V				
	W				
	X				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.